Simulation and Design of Solar Feed EZ-Source Inverter

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Abstract: *Z-Source inverters are used mainly for buck-boost energy conversion with the help of passive elements. In Z-source inverter topology, impedance network is used to couples the source and the inverter to achieve voltage boost and inversion. Embedded EZ-Source inverter and Z-Source inverter can produce the same gain. Input to the Embedded EZ-Source inverter can be obtained from solar cell. The ripple content in the output voltage of solar cell is filtered and pure DC is given to the three-phase inverter which converts DC in to three-phase balanced AC. The output of the Embedded EZ Source inverter is used to control the harmonics present in the load. The limitations of the conventional Z-source inverter can be overcome by using Embedded Z-source inverters. It can produce smoother current or voltage across the dc input source and it has low harmonic distortions. Simulations are carried out using MATLAB-SIMULINK. Hardware implementation and Microcontroller programming can be done in the lab.*

Keywords: EZ-source inverters, voltage boost, Z-Source inverter and Pulse Width Modulation (PWM), Total Harmonic Distortion (THD).

I. INTRODUCTION

The circuit which is used to convert the direct current (DC) input signal into alternating current (AC) output signal is called as inverter. Traditionally there exist two types of inverters namely Voltage Source Inverter, Current Source Inverter. For dc to ac and ac to dc power conversion voltage source inverter performs buck and boost operation respectively. In the case of current source inverter it performs buck operation for ac to dc power conversion and boost operation for dc to ac power conversion that is both the V-source converter and the I-source converter have the common problem that they can operate either a boost or a buck converters and cannot be a buck–boost converter. The limitations of the traditional voltage source and current source converters can be eliminated by introducing Z-source inverter. Z-Source inverter utilize LC impedance network which performs both buck-boost energy conversions. It can be used in implementing dc-to-ac, ac-to-dc, ac-to-ac, and dc-to-dc power conversion. The Z-source network boosts the input voltage level for inverter and also provides filtered output. Thus this technique improves efficiency of inverter. The structure Z-source consist two inductors and two capacitors arranged in X shape for buck boost operation in single stage conversion. To avoid chopping in source current an additional LC filter is placed before diode therefore the cost of system would rise and due to addition of LC filter the system becomes more complex. In the case of EZ source inverter, source is places in series with inductors which smoothens the source current and chopping currents get filtered without any additional LC filter. For system design of EZ-Source inverter two PV panels are required. These PV panels work as a dc source which embedded within LC impedance network. The two DC source generate varying DC voltages whose values are depend on atmospheric conditions. Voltage and current filtering can be done by using LC components.

II. Z-SOURCE INVERTER

Fig.1. shows the circuit of Z-source inverter where the input dc source and three-phase inverter bridge is connected by X-shape network which is LC impedance network. Any two switches from the same phase-leg of inverter can be turned on safely to introduce a shoot-through state which is nothing but the short-circuit state. This condition can be brought by addition of LC impedance network. The inductive element (L1, L2, or both) used to limit the current paths from dc front-end. Due to insertion of shoot-through state, the Z-source inverter can provides voltage-boosting capability. Consider the inverter-state equations during shoot-through and non shoot-through states, expressed by (1) and (4) with a balanced network assumed (L1 = L2 = L and C1 = C2 = C). These equations are used to derive the gain of inverter.
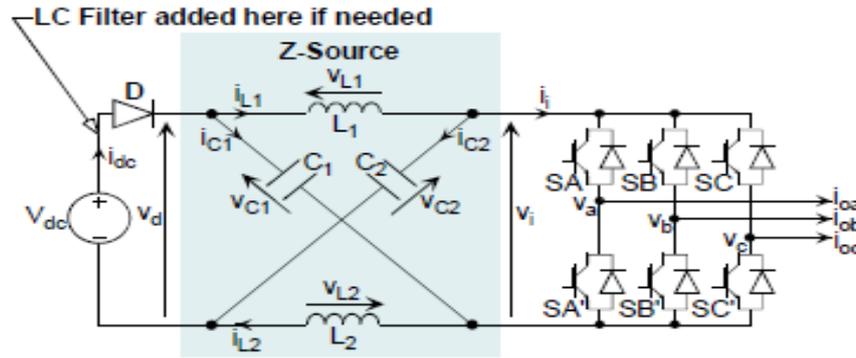

Fig.1. Z-source inverter

Conditions of Shoot-Through state:
(Sx = Sx1 = ON, x = A, B, or C; D = OFF)
vL =VC; vi = 0; vd = 2VC; vD = Vdc − 2VC---- (1)
iL = − Ic; ii = iL − iC; idc = 0---- (2)
Conditions of Non Shoot-Through state:
(Sx ≠Sx1, x = A, B, or C; D = ON)
vL =Vdc − VC ;vi = 2VC – Vdc; vd = Vdc; vD = 0 ---(3)
idc =iL + iC; ii = iL – iC; idc ≠ 0--- (4)
The following expressions are for VC capacitive voltage, peak dc-link voltage ^vi, and peak ac output voltage vx.
VC =(1 − T0/T)/(1 − 2T0/T) *Vdc-----(5)
^vi = Vdc / (1 − 2T0/T)= BVdc------(6)
vx =M vi/2= B(M Vdc/2)------(7)
where T0/T denotes the shoot-through ratio (T0/T < 0.5) per switching period, M is the modulation index used for traditional inverter control, and B is boost factor given by expression B = 1/(1 − 2T0/T).

III. EMBEDDED EZ-SOURCE INVERTER

Fig.2. shows the circuit of Embedded EZ-Source inverter. It consist L1 and L2 inductive elements and C1 and C2 capacitive elements which forms LC impedance network.
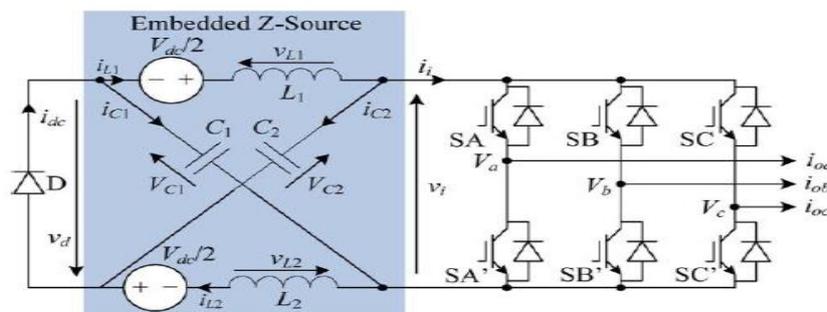

Fig.2. EZ-source inverter

Two DC sources embedded within the X-shaped LC impedance network. In the case of voltage type EZ-source inverter inductive elements L1 and L2 used for filtering the currents and in case of current type EZ-source inverters capacitive elements C1 and C2 used for voltage filtering in current type EZ-source inverters keeping the voltage or current gain of the inverter constant. The input DC can

be taken from the solar cell given to the Z-source. The filtered, ripple free, pure DC given to the three phase inverter which converts pure DC into three phase balanced AC.

IV.EXPERIMENTAL RESULTS

The simulation of proposed system is done in MATLAB SIMULINK. The output voltage from two DC sources fed to the Z filter of Embedded EZ-Source Inverter. The filtered DC output from Z filter is given to the Embedded EZ-Source Inverter which converts pure DC into three phase balanced AC.

The simulation circuit diagram of Embedded EZ-Source inverter is shown in fig.3. The triggering pulses for switches of Embedded EZ-Source inverter is shown in fig.4. The line current waveform is shown in fig.5, fig.6 and fig.7 represents line voltage waveforms. The output from the Embedded EZ-Source Inverter is given to the induction motor which is asynchronous machine. Three phase induction motor is taken as a load. Fig.8 shows the plot of rotor speed of induction motor with respect to time. It represents the speed of induction motor increases and settles down above 1600 RPM (Revolution Per Minute). For calculation of total harmonic distortion FFT analysis for current of the system is taken. It shows that THD value is 3.93% shows in fig.9. Thus total harmonic distortion is low in Embedded EZ-Source inverter system
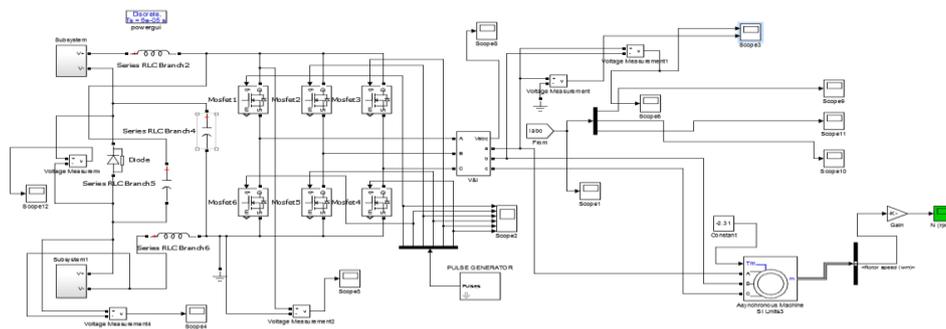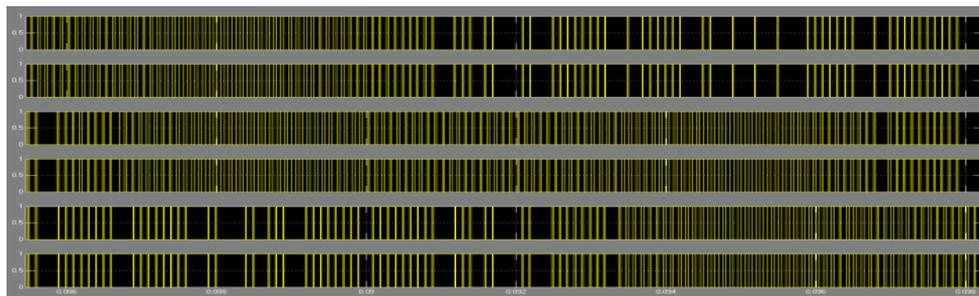

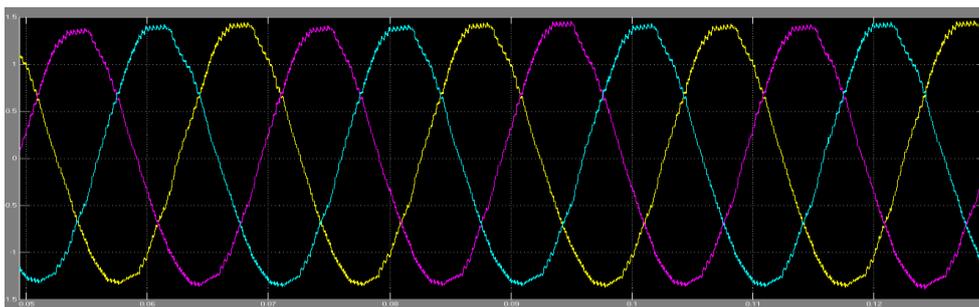Fig.3. Simulation circuit diagram

Fig.4. Triggering pulses

Fig.5. Line current waveform

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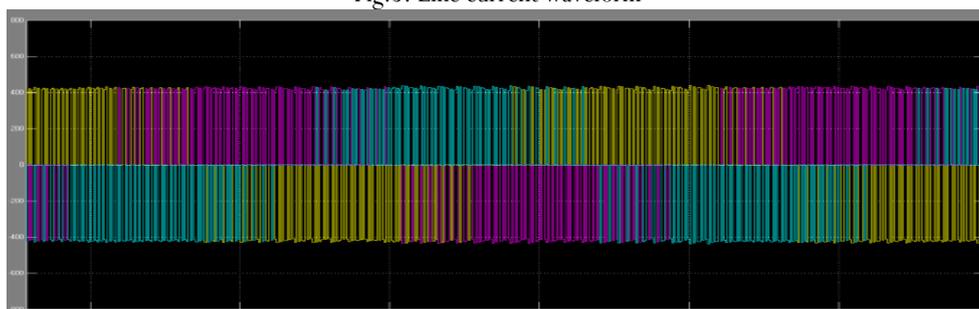
Fig.6. Line voltage waveform

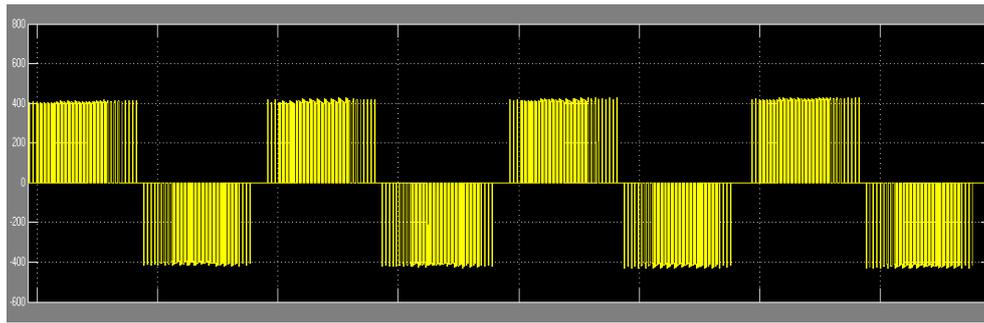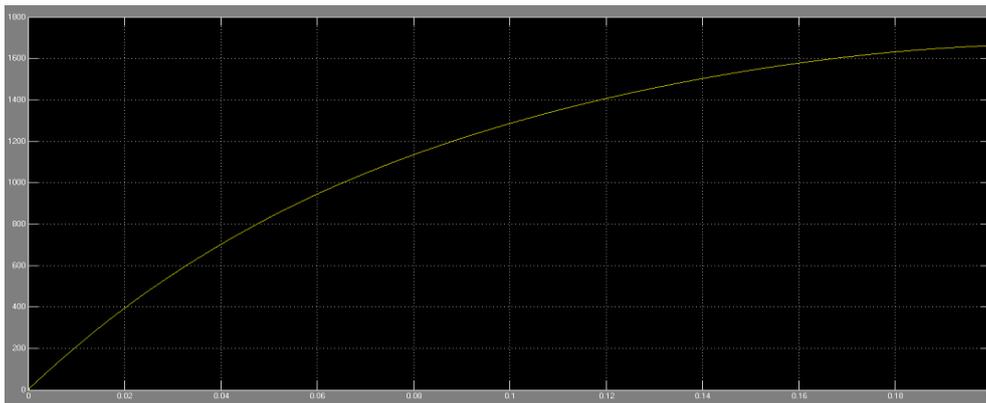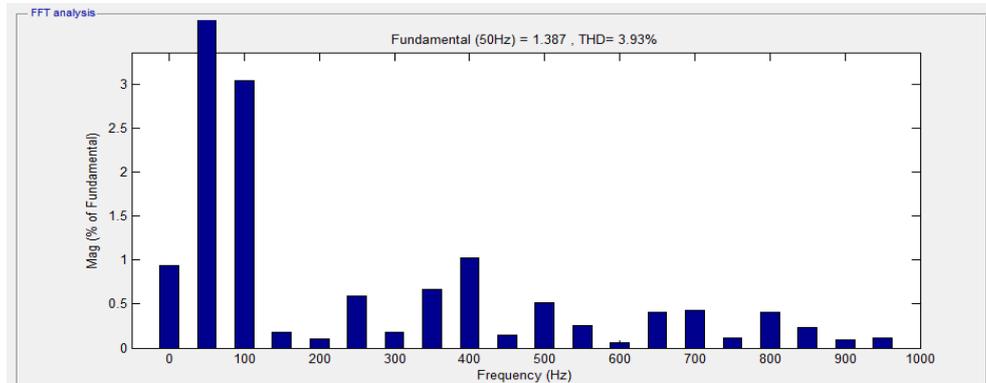Fig.7. Phase to Phase voltage waveform

Fig.8. Motor Rotor speed

Fig.9. FFT analysis and total harmonic distortion (THD)
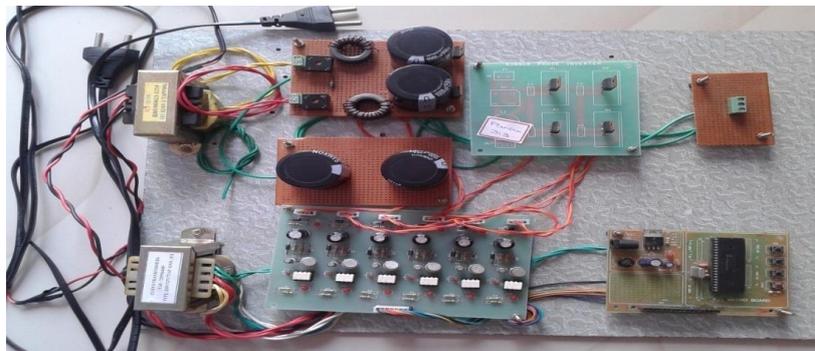
Hardware Setup of EZ-Source Inverter

Fig.10 Proposed system hardware

Fig.10 shows the complete hardware setup of EZ-source inverter. It consist power supply unit, Z-source impedance network, induction motor as a load, driver circuit for inverter and 6 switches three phase inverter. Projections for solar input are provided in the system. DC Input for the Z-source is provided by the solar plates, filtered DC from Z-source fed to three phase inverter which generate balanced AC to run induction motor. The Microcontroller PIC16F877A used to generate control pulses for three phase inverter. The driver circuit is used to amplify these triggering pulses and given to the inverter. The balanced three phase ac provided by three phase inverter which then given to the motor loads.

V. CONCLUSION

The simulation of EZ-Source inverter system is carried out in MATLAB SIMULINK software. From the experimental results are presented in this paper, we observed that the given system provides voltage boosting and current filtering using impedance network also it has advantages of reduced harmonic distortions of about 3.93%. Its hardware implementation has been done and results are obtained by using PV panels at input and motor load at the output. Thus EZ-source inverter has great advantages over current source and voltage source inverters.

VI. ACKNOWLEDGMENT

VII. REFERENCES

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